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Applicants: Donald B. Eidson, et al.
Serial No.: 09/602,690
For: RATE N/N SYSTEMATIC, RECURSIVE
CONVOLUTIONAL ENCODER AND CORRESPONDING
DECODER
Filed: June 23, 2000

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT FORM
1449

Attorney(s): Robert C. Laurenson
Docket No.: 01827.0037 US00
Date of Deposit: August 28, 2000, via First Class Mail
Enclosure(s): Information Disclosure Statement; PTO-1449; References (7);
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Donald B. Eidson, et al.

Application No.: 09/602,690

Filed: June 23, 2000

For: RATE N/N SYSTEMATIC, RECURSIVE
CONVOLUTIONAL ENCODER AND
CORRESPONDING DECODER

Art Unit: 2734

Examiner: Unassigned

1c903 U.S. PTO
09/887877
06/21/01

Information Disclosure Statement

Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant hereby submits, without admission of prior art effect thereof, the documents listed on the accompanying Form PTO-1449 pursuant to the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Applicant has listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

CERTIFICATE OF MAILING
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached hereto) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

August 28, 2000
Date of Deposit

Myritza O'Neill
Name of Person Mailing Paper

Signature of Person Mailing Paper


This Information Disclosure Statement is being filed within three months of the U.S. filing date or before the mailing date of a first Office Action on the merits, therefore no statement under 37 C.F.R. § 1.97(e) or fee is required.

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and to indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 08-3038.

Respectfully submitted,

Date: August 25, 2000


Robert C. Laurenson (Reg. No. 34,206)

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FORM PTO-1449 LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 01827.003 / J500	SERIAL NO. 09/602,690
	APPLICANT: Donald B. Eidson, et al.	
	FILING DATE: June 23, 2000	GROUP: 2734

10903 U.S. PTO
 09/887877
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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
				None			

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
				None			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
	Benedetto, S. et al., <i>A Soft-Input Soft-Output Maximum A Posteriori (MAP) Module to Decode Parallel and Serial Concatenated Codes</i> , TDA Progress Report 42-127, November 15, 1996, pages 1-20.
	Benedetto, S. et al, <i>Serial Concatenated Trellis Coded Modulation with Iterative Decoding: Design and Performance</i> , Dipartimento di Electronica, Politecnico di Torino, and Jet Propulsion Laboratory, California Institute of Technology, 1997 IEEE, pages 38-43.
	Benedetto, Sergio, <i>Turbo Codes: Analysis, Design, Iterative Decoding and Applications</i> , International Courses for Telecom and Semiconductor Professionals, Barcelona, Spain, October 25-29, 1999, 4 pages.
	Berrou, Claude et al., <i>Near Shannon Limit Error - Correcting Coding and Decoding: Turbo-Codes (1)</i> , Integrated Circuits for Telecommunication Laboratory et al., 1993 IEEE, pages 1064 - 1070.
	Divsalar, D. et al., <i>Turbo Trellis Coded Modulation with Iterative Decoding for Mobile Satellite Communications</i> , Jet Propulsion Laboratory, California Institute of Technology, 4800 Oak Grove, Dr., Pasadena, CA 91109, (7 pages).
	Hoehner, Peter et al., <i>"Turbo DPSK": Iterative Differential PSK Demodulation and Channel Decoding</i> , IEEE Transactions on Communications, Vol. 47, No. 6, June, 1999, (15 pages).
	Narayanan, Krishna R., et al., <i>A Serial Concatenation Approach to Iterative Demodulation and Decoding</i> , IEEE Transactions on Communications, Vol., 47, No. 7, July, 1999, pages 956 -

EXAMINER:	DATE CONSIDERED:
EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant	